Royal College of Arts, Science and Commerce
F.Y.B.Sc Computer Science
Semester I 2020-2021
Computer Organization and Design (Course code: - USCS101)
Sample Questions

Q1)   Which functional unit of computer is used to store data or information?

a) Input unit
b) Output unit
c) Memory unit
d) Control unit

Q2)   ALU + _____ = CPU.

a) MU
b) CU
c) Register File
d) Input device

Q3)   A 8:1 multiplexer has _____number of select inputs.

a) 3
b) 4
c) 2
d) 1

Q4)   Binary number 00001011 is equal to _____hexadecimal.

a) A0
b) AB
c) 0F
d) 0B

Q5)   Decimal number 35 is equal to _____Octal.

a) 34
b) 43
c) 4
d) 3

Q6)   In Data path _____number of phases or stages available.

   a) 4
   b) 5
   c) 2
   d) 3

Q7)   Tri-State buffer contains output f same as input x if e = _____.

   a) 1
   b) 0
   c) Same as x
   d) Same as f

Q8)   A CPU generally handles an interrupt by executing an ISR.

   a) By checking an interrupt register at fixed intervals.
   b) ISR will be executed as soon as Interrupt is raised.
   c) By checking an interrupt register at the end of fetch cycle.
   d) By checking an interrupt register after finishing the execution of current instruction.

Q9)   How much address space we get if computer has n bit address?

   a) 2^n
   b) 2*n
   c) (2^n)-1
   d) n+1

Q10)   Difference between DIV R2, R3 and IDIV R3,R2 is _____.

   a) Both divides R3/R2
   b) IDiv instruction does R2/R3 and generates only Quotient
   c) Div instruction does R2/R3 and generates both Quotient and remainder
   d) IDiv instruction does R2/R3 and generates both Quotient and remainder

Q11)   In state diagram, states are represented as _____and states are connected through _____.

   a) Rectangle, arrows
   b) Circle, labelled arrows.
   c) Oval , arrows
   d) arrows , circle

Q12)   _____instruction reads contents of memory location loc1 and store the value in R2.

a) Store R2,Loc1
b) Load R2,Loc1
c) Store Loc1 , R2
d) Load Loc1,R2

Q13)   RISC code is _____ .

a) lengthy than CISC
b) Smaller than CISC
c) Complex than CISC
d) Reduced than CISC

Q14)   The routine executed in response to an interrupt request is called as _____.

a) IER
b) ISR
c) SR
d) IR

Q15)   Computer understands machine language and Job of Assembler is to _____.

a) Convert machine code to used instructions.
b) Convert assembly code into object code
c) Convert Object code into assembly code
d) convert source into Mnemonics code